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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jong-Hoon Oh

Docket No.: 2003P52610US/I331.111.101

Patent No.: 6,909,660 B2

Issued: June 21, 2005

Title: RANDOM ACCESS MEMORY HAVING DRIVER FOR REDUCED
LEAKAGE CURRENT**COMMUNICATION RE: CERTIFICATE OF CORRECTION**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450Certificate
AUG 18 2005
of Correction

Dear Sir/Madam:

Applicant hereby requests an Official Certificate of Correction with respect to the above-identified Patent. It is believed that the errors listed on the enclosed Certificate of Correction are on the part of the U.S. Patent and Trademark Office, and therefore no fees are due in connection with this matter. The Patent Office is authorized to charge any fee that may be due, however, to Deposit Account No. 500471 to ensure prompt issuance of a Certificate of Correction.

Enclosed for the convenience of the Patent Office is a copy of the Notice of Recordation of Assignment Document, indicating the correct Assignee information.

Respectfully submitted,

Jong-Hoon Oh,

By his attorneys,

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SED:masBy: Steven E. Dicke
Steven E. Dicke
Reg. No. 38,431

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service, as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 11th day of August, 2005.

By: Steven E. Dicke
Name: Steven E. Dicke

AUG 19 2005

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,909,660 *B2* **Page 1 of 1**
DATED : June 21, 2005
INVENTORS : Oh

It is certified that error appears in the above-identified patent and that said Letter Patent is hereby corrected as shown below:

Column 3, line 45, "10" has been changed to --110--.

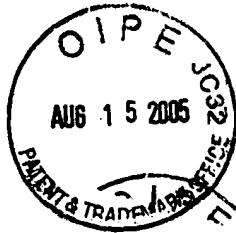
Front page, (73) Assignee: "Infineon Technologies North America Corp., San Jose, CA (US)" has been changed to --Infineon Technologies AG, Munich, Fed Rep Germany--

MAILING ADDRESS OF SENDER:
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PATENT NO. 6,909,660
Docket No.: 2003P52610US/I331.111.101

No. of add'l copies @ .50 per page: 0

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DUE:

UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF RECORDATION OF ASSIGNMENT DOCUMENT

THE ENCLOSED DOCUMENT HAS BEEN RECORDED BY THE ASSIGNMENT DIVISION OF THE U.S. PATENT AND TRADEMARK OFFICE. A COMPLETE MICROFILM COPY IS AVAILABLE AT THE ASSIGNMENT SEARCH ROOM ON THE REEL AND FRAME NUMBER REFERENCED BELOW.

PLEASE REVIEW ALL INFORMATION CONTAINED ON THIS NOTICE. THE INFORMATION CONTAINED ON THIS RECORDATION NOTICE REFLECTS THE DATA PRESENT IN THE PATENT AND TRADEMARK ASSIGNMENT SYSTEM. IF YOU SHOULD FIND ANY ERRORS OR HAVE QUESTIONS CONCERNING THIS NOTICE, YOU MAY CONTACT THE EMPLOYEE WHOSE NAME APPEARS ON THIS NOTICE AT 703-308-9723. PLEASE SEND REQUEST FOR CORRECTION TO: U.S. PATENT AND TRADEMARK OFFICE, ASSIGNMENT DIVISION, BOX ASSIGNMENTS, CG-4, 1213 JEFFERSON DAVIS HWY, SUITE 320, WASHINGTON, D.C. 20231.

RECORDATION DATE: 06/10/2004

REEL/FRAME: 014716/0270
NUMBER OF PAGES: 6

BRIEF: ASSIGNMENT OF ASSIGNOR'S INTEREST (SEE DOCUMENT FOR DETAILS).

ASSIGNOR:

INFINEON TECHNOLOGIES NORTH
AMERICA CORP.

DOC DATE: 06/09/2004

ASSIGNEE:

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MUNICH, FED REP GERMANY 81669

SERIAL NUMBER: 10674177 2003P52606US
PATENT NUMBER:

FILING DATE: 09/29/2003
ISSUE DATE:

TITLE: RANDOM ACCESS MEMORY WITH POST-AMBLE DATA STROBE SIGNAL NOISE
REJECTION

SERIAL NUMBER: 10675549 2003P52595US
PATENT NUMBER:

FILING DATE: 09/30/2003
ISSUE DATE:

TITLE: ECHO CLOCK ON MEMORY SYSTEM HAVING WAIT INFORMATION

014716/0270 PAGE 2

SERIAL NUMBER: 10673626 *2003P5240005* FILING DATE: 09/29/2003

PATENT NUMBER: ISSUE DATE:

TITLE: MEMORY DEVICE WITH A FLEXIBLE REDUCED DENSITY OPTION

SERIAL NUMBER: 10672120 *2003P5259905* FILING DATE: 09/26/2003

PATENT NUMBER: ISSUE DATE:

TITLE: MEMORY DEVICE HAVING MULTIPLE ARRAY STRUCTURE FOR INCREASED BANDWIDTH

SERIAL NUMBER: 10672118 *2003P5241205* FILING DATE: 09/26/2003

PATENT NUMBER: ISSUE DATE:

TITLE: RANDOM ACCESS MEMORY HAVING DRIVER FOR REDUCED LEAKAGE CURRENT

SERIAL NUMBER: 10453226 *2002P5000305* FILING DATE: 06/02/2003

PATENT NUMBER: ISSUE DATE:

TITLE: MULTITHREADED PROCESSOR WITH MULTIPLE CACHES

SERIAL NUMBER: 10690538 *2004P5047805* FILING DATE:

PATENT NUMBER: ISSUE DATE:

TITLE:

SERIAL NUMBER: 10674386 *2003P5232105* FILING DATE: 10/01/2003

PATENT NUMBER: ISSUE DATE:

TITLE: SYSTEM AND METHOD FOR AUTOMATICALLY-DETECTING SOFT ERRORS IN LATCHES OF AN INTEGRATED CIRCUIT

SERIAL NUMBER: 10678837 *2003P5273405* FILING DATE: 10/03/2003

PATENT NUMBER: ISSUE DATE:

TITLE: LDMOS TRANSISTOR

SERIAL NUMBER: 10253909 *2002P1355305* FILING DATE: 09/25/2002

PATENT NUMBER: ISSUE DATE:

TITLE: MODULATION CODING BASED ON AN ECC INTERLEAVE STRUCTURE

SHAREILL COLES, EXAMINER
ASSIGNMENT DIVISION
OFFICE OF PUBLIC RECORDS